

**Micro Commercial Components** 

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## DTA144EE

## **Features**

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy

### Absolute maximum ratings @ $25^{\circ}$ C

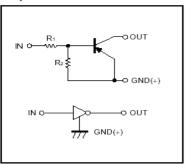
Symbol	Parameter	Min	Тур	Max	Unit
$V_{cc}$	Supply voltage		-50		V
$V_{IN}$	Input voltage	-40		10	V
I <sub>O</sub> I <sub>C(MAX)</sub>	Output current		-30 -100		mA
$P_d$	Power dissipation		150		mW
Tj	Junction temperature		150		$^{\circ}$
$T_{stg}$	Storage temperature	-55		150	$^{\circ}\mathbb{C}$

 Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0 and MSL Rating 1

### **Electrical Characteristics @ 25°**℃

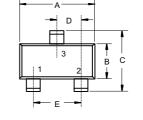
Symbol	Parameter	Min	Тур	Max	Unit
$V_{I(off)}$	Input voltage (V <sub>CC</sub> =-5V, I <sub>O</sub> =-100 μ A)		-	-0.5	V
$V_{I(on)}$	$(V_0=-0.3V, I_0=-2mA)$	-3.0			V
$V_{O(on)}$	Output voltage (I <sub>O</sub> /I <sub>I</sub> =-10mA/-0.5mA			-0.3	V
l <sub>l</sub>	Input current (V <sub>I</sub> =-5V)			-0.18	mA
I <sub>O(off)</sub>	Output current (V <sub>CC</sub> =-50V, V <sub>I</sub> =0)			-0.5	μА
Gı	DC current gain (V <sub>0</sub> =-5V, I <sub>0</sub> =-5mA)	68			
R <sub>1</sub>	Input resistance	32.9	47	61.1	$\mathbf{K}\Omega$
R <sub>2</sub> /R <sub>1</sub>	Resistance ratio	0.8	1.0	1.2	
f <sub>T</sub>	Transition frequency (V <sub>CE</sub> =-10V, I <sub>E</sub> =5mA, f=100MHz)		250		MHz

### ●Equivalent circuit

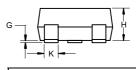


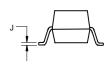
# PNP Digital Transistors











DIMENSIONS					
	INC	HES	MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.059	.067	1.50	1.70	
В	.030	.033	0.75	0.85	
C	.057	.069	1.45	1.75	
D	.020 Nominal		0.50Nominal		
Е	.035	.043	0.90	1.10	
G	.000	.004	.000	.100	
Н	.028	.031	.70	0.80	
J	.004	.008	.100	.200	
K	.010	.014	.25	.35	

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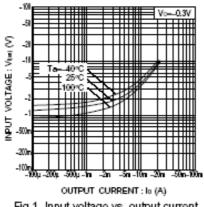


Fig.1 Input voltage vs. output current (ON characteristics)

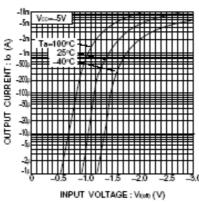


Fig.2 Output current vs. input voltage (OFF characteristics)

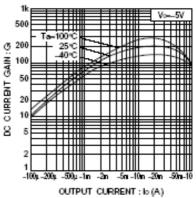


Fig.3 DC current gain vs. output current

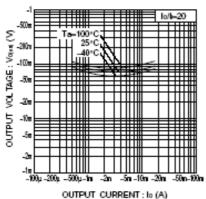


Fig.4 Output voltage vs. output

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## **Ordering Information**

Device	Packing
(Part Number)-TP	Tape&Reel3Kpcs/Reel

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